STANDARDS CHANGES CATALOG (SCC)

SCC NUMBER: SCC #155

CHANGE PROPOSAL TITLE: Address format Clarification, Paragraph 5.3.4.2.2.1

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ORIGINATOR'S INTERNAL NUMBER:

AFFECTED DOCUMENT: MIL-STD-188-220C, Paragraph 5.3.4.2.2.1

PRECEDENCE: Routine

RECOMMENDATIONS:

RECORD OF PROCESSING

DATE:	ACTION:
22 Jan 04	Proposal/Work Item
29 Jan 04	Draft/Approved for MIL-STD-188-220C

1. STATEMENT OF THE PROBLEM:
The Address format described in Paragraph 5.3.4.2.2.1 is not described correctly.

2. PROBLEM ANALYSIS:
The sentence “Single octet and four octets shall not be mixed in the same net.” in Paragraph 5.3.4.2.2.1 is not described correctly.

3. PROPOSED SOLUTION:

Change the sentence “Single octet and four octets shall not be mixed in the same net.” in Paragraph 5.3.4.2.2.1. to “Single octet and four octets addressing for individual addresses (see paragraph 5.3.4.2.2.2.2.4) shall not be mixed in the same net.”.

4. ALTERNATIVE SOLUTIONS: None.

5. SYSTEM CHANGES REQUIRED: None.

6. CONFIGURATION ITEM DOCUMENTATION CHANGES:
MIL-STD-188-220C, Paragraph 5.3.4.2.2.1.~~.~~, Appendix B, page 118, item 204.2.2.1.b.

7. IMPACT ON INTEROPERABILITY: None.

8. IMPACT ON RELATED DOCUMENTS: None.

9. IMPLEMENTATION DATES: ~~TBD~~ Upon approval of the SCC.

10. OTHER CONSIDERATIONS: None.

11. REFERENCES: None.

12. Trouble Reports (TRs) ADDRESSED IN THIS SCC: None.

204.2.2.1.a	Single octet addressing shall be mandatory and four octet addressing is optional for synchronous, asynchronous, and packet modes of operation.	5.3.4.2.2.1	M	Yes__ No__	
204.2.2.1.b	Single octet and four octets addressing for individual addresses (see paragraph 5.3.4.2.2.2.2.4) shall not be mixed in the same net.~~Single octet and four octets shall not be mixed in the same net.~~	5.3.4.2.2.1	M	Yes__ No__	
204.2.2.1.1	Single Octet Addressing	5.3.4.2.2.1.1	M	Yes__ No__	
204.2.2.1.1.a	Each address in the address fields shall consist of a single octet	5.3.4.2.2.1.1	M	Yes__ No__	
204.2.2.1.1.b	The source address octet shall consist of a command/response (C/R) designation bit (the LSB) followed by a 7-bit address representing the source	5.3.4.2.2.1.1	M	Yes__ No__	
204.2.2.1.1.c	Each destination octet shall consist of an extension bit (the LSB) followed by the 7-bit destination address. The destination address uses a modification of the HDLC extended addressing format	5.3.4.2.2.1.1	M	Yes__ No__	
204.2.2.1.1.d	The destination address shall be extended by setting the extension bit of a destination address octet to 0, indicating that the following octet is another destination address	5.3.4.2.2.1.1	M	Yes__ No__	
204.2.2.1.1.e	The destination address field shall be terminated by an octet that has the extension bit set to 1	5.3.4.2.2.1.1	M	Yes__ No__	
204.2.2.1.1.f	The destination address field shall be extendible from 1 address octet to 16 address octets	5.3.4.2.2.1.1	M	Yes__ No__	
204.2.2.1.1.g	The format of the address fields shall be in the extended address field format	5.3.4.2.2.1.1	M	Yes__ No__	